



PATENT  
81912.0014  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Satoshi YANAGISAWA

Serial No: 10/643,717

Filed: August 19, 2003

For: Semiconductor Device with Semiconductor  
Chip Mounted in Package

Art Unit: 2826

Examiner: Andujar, Leonardo

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. 1.136, Applicant respectfully petitions the Commissioner for a two-month extension of time extending to April 14, 2006, the period for response to the Final Office Action dated November 14, 2005. The extension is sought for the purpose of maintaining pendency so that a divisional application of this case may be filed. A check for \$450.00 is enclosed.

Please charge any insufficiency or credit any overpayment to Deposit Account No. 50-1314. A copy of this petition is enclosed.

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450.00 0P

Date: April 11, 2006

Respectfully submitted,

HOGAN & HARTSON L.L.P.

By:

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